INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(PTO-1449)

ATTY. DOCKET NO.	SERIAL NO.
066365-0013	**10/813,628**

APPLICANT
Simon KNOWLES

FILING DATE	GROUP
March 31, 2004	**2183**

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code2 (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
W	A1	US	2003/154358	08/14/2003	Seong Nak-Hee et al.	
W	A	US	5,600,810	02/04/1997	Ohkami et al.	
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codea -Number 4 -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes	Translation No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
W		Beebe B et al., "Instruction Sequencing Control" IBM Technical Disclosure Bulletin, IBM Corp., New York, US, XP–001032127	
W		International Search Report dated May 19, 2006	

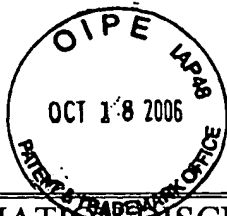| EXAMINER	DATE CONSIDERED
[signature]	11-27-06

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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W		US	5,600,810	02-04-1997	Takahide Ohkami	
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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W		BEEBE, B.O. ET AL., "Instruction Sequencing Control," IBM Technical Disclosure Bulletin, May 19, 1972, pp. 3599-3611, vol. 14, no.12, XP001032127	

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W		US	5,423,051	06-06-1995	FULLER et al.	
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W		BOLOTSKI, et al., "Unifying FPGAs and SIMD Arrays," M.I.T. Transit Project, February 8, 1994, pp. 1-22, Transit Note #95.	
W		STOKES, "A Brief Look at the PowerPC 970," Ars Technica, 2002, pp. 1-3.	
W		ALIPPI, et al., "Determining the Optimum Extended Instruction-Set Architecture for Application Specific Reconfigurable VLIW CPUs," IEEE, 2001, pp. 50-56.	

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